

Notice of References Cited	Application/Control No. 10/776,064	Applicant(s)/Patent Under Reexamination KIM ET AL.	
	Examiner Kevin Quinto	Art Unit 2826	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-5,717,251	02-1998	Hayashi et al.	-
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber, "Silicon Processing for the VLSI Era, Volume 1 - Process Technology," 2000, Lattice Press, vol. 1, p.527-531
	V	Wolf, "Silicon Processing for the VLSI Era, Volume 2 - Process Integration," 1990, Lattice Press, vol. 2, p.273-276
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.